

Remarks

Applicant respectfully requests reconsideration of this application as amended. Claim 1 has been amended. No claims have been cancelled. Therefore, claims 1-5, 7-16, 18-27 and 29-33 are presented for examination.

Claims 1-5, 7-16, 18-27, and 9-33 stand rejected under 35 U.S.C. §101 because the claimed invention is directed to non-statutory subject matter. Applicant submits that claim 1 has been amended to appear in proper condition for allowance.

Claims 1-5, 7-16, 18-27, and 29-33 stand rejected under 35 U.S.C. §102(e) as being anticipated by Sih et al. (U.S. Patent No. 6,606,700). Applicant submits that the present claims are patentable over Sih.

Sih discloses a digital signal processor architecture that is designed to speed up frequently-used signal processing computations, such as FIR filters, correlations, FFTs, and DFTs. The architecture uses a coupled dual-MAC architecture and attaches a dual-MAC coprocessor onto it in such a way as to achieve an increase in processor capability. (Sih at col. 1, ll. 49-55.)

Claim 1 of the present application recites saving input data at one or more buffers within an execution unit for one or more multiply-accumulate operations to be performed during a subsequent computational cycle. Applicant submits that nowhere in Sih is there disclosed, or reasonably suggested, an *execution unit having buffers to save data to perform multiply-accumulate operations during a subsequent computational cycle*. Therefore, claim 1, as well as its dependent claims, is patentable over Sih.

Independent claims 10 and 21 each recite an execution block having one or more buffers to save input data received at one or more of the inputs, the data to be utilized by the

execution unit in a subsequent multiply-accumulate operations after current multiply-accumulate operations. As discussed above, Sih does not disclose or suggest such features. Therefore, claims 10 and 21, as well as their respective dependent claims, are patentable over Sih for the reasons discussed above with respect to claim 1.

Applicant submits that the rejections have been overcome and that the claims are in condition for allowance. Accordingly, applicant respectfully requests the rejections be withdrawn and the claims be allowed.

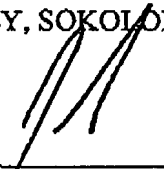
The Examiner is requested to call the undersigned at (303) 740-1980 if there remains any issue with allowance of the case.

Please charge any shortage to our Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: 5/7/07



Mark L. Watson
Reg. No. 46,322

12400 Wilshire Boulevard
7th Floor
Los Angeles, California 90025-1026
(303) 740-1980